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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Perino *et al.*

Confirmation No.: 4507

Serial No. 09/961,114

Group Art Unit: 2833

Filed: September 20, 2001

Examiner: Figueroa, Felix O.

For: *Chip Socket Assembly and Chip File  
Assembly for Semiconductor Chips*

Attorney Docket No.: 60809-5054-US  
(formerly RB1-035USC3)

RESPONSE TO FINAL OFFICE ACTION

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

This response is in reply to the final Office Action from the U.S. Patent and Trademark Office dated March 21, 2005. Please enter the following amendments and remarks into the file of the above identified application.

Pursuant to the ultimate paragraph of this amendment, the Commissioner is authorized to charge any required fees to Morgan, Lewis & Bockius LLP Deposit Account No. 50-0310 (order No. 60809-5054-US).